

Model: DPPR-0123

FPGA Development Board

A Product of DPPR Systems (P) Ltd.

An ISO 9001:2008 Certified Company

FPGA development Board provides a convenient development board for embedded processing applications. It supports various interface like RS232, USB, LCD, PS/2 etc. The board highlights these features:

- ❖ **Spartan-IIE 1.8V FPGA Family(300000 Gate,64 Block RAM,329 user IOs, PQFP package etc)**
- ❖ **2 x 16 LCD display screen**
- ❖ **PS/2 mouse**
- ❖ **PS/2 keyboard port**
- ❖ **VGA display port**
- ❖ **Two 9-pin RS-232 ports (DB9)**
- ❖ **USB Port**
- ❖ **On board JTAG header for programming and debugging**
- ❖ **50 MHz crystal oscillators**
- ❖ **Onboard 3.3V and 1.8V Regulator**
- ❖ **36 IOs(LVTTL/TTL) for interfacing other devices**

We have list of projects based on FPGA which can be implemented in the Spartan-IIE 1.8V FPGA development board.

- ❖ **Implementation of UART inside FPGA ([manual_1.pdf](#))**
- ❖ **Implementation of VGA core inside FPGA ([manual_2.pdf](#))**
- ❖ **Implementation of LCD Display core inside FPGA ([manual_3.pdf](#))**
- ❖ **Implementation of VGA, Keyboard core inside FPGA ([manual_4.pdf](#))**
- ❖ **Implementation of LCD Display, Keyboard core inside FPGA ([manual_5.pdf](#))**
- ❖ **Implementation of UART and SD Card core inside FPGA ([manual_6.pdf](#))**
- ❖ **Implementation of LCD, Key board and SD Card core inside FPGA ([manual_7.pdf](#))**
- ❖ **Implementation of VGA, Key board and SD Card core inside FPGA ([manual_8.pdf](#))**
- ❖ **Implementation of UART and LCD Display core inside FPGA ([manual_9.pdf](#))**
- ❖ **Implementation of UART and VGA core inside FPGA ([manual_10.pdf](#))**
- ❖ **Implementation of USB protocol inside FPGA ([manual_11.pdf](#))**
- ❖ **Frequency and Phase Measurement using FPGA ([manual_12.pdf](#))**

(Above motioned projects can be implemented in any FPGA development board)

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